

# JS120KQ

## **Anti-parallel Module**

#### Rev.1.0 Feb.10 2025

#### Description

- 1) A package consists of two inverse parallel SCR chips, which rated voltage is up to 1800V
- 2) Welding by vacuum welding technology, which provide high reliability
- 3) Insulated by silicone gel, provide a insulation voltage of 3000V~
- 4) UL 1557 item recognized. (File No.: E252906)

#### **Typical Application**

Soft start, solid state relay, AC/DC switch, temperature control.



Parameter	Test Conditions	Symbol	Values			
			12	16	18	Unit
Operating junction temperature range		TJ	-40-125			°C
Storage temperature range		Tstg	-40-125			°C
Repetitive peak off-state voltage	T <b>J=25°</b> ℃	Vdrm	1200	1600	1800	V
Repetitive peak reverse voltage	<b>T</b> J <b>=25</b> ℃	Vrrm	1200	1600	1800	V
Non-repetitive peak off-state voltage	T <b>J=25</b> ℃	Vdsm	1300	1700	1900	V
Non-repetitive peak reverse voltage	T <b>J=25</b> ℃	Vrsm	1300	1700	1900	V
RMS on-state current	Tc <b>=80</b> ℃	I <sub>T(RMS)</sub>	135			А
Peak on-state surge current	t <sub>P</sub> =10ms V <sub>R</sub> =0.6V <sub>RRM</sub>	I <sub>TSM</sub>	2000			А
l <sup>2</sup> t value for fusing	t <sub>P</sub> =10ms V <sub>R</sub> =0.6V <sub>RRM</sub>	l <sup>2</sup> t	20000			A <sup>2</sup> s
Critical rate of rise of on-state current	I <sub>G</sub> =2×I <sub>GT</sub>	di/dt	150			A/µs
Insulation voltage	A.C 50Hz(1s/1min)	Viso	3000/2500			V

#### Absolute Maximum Ratings (Packaged into modules, unless otherwise specified, TCASE=25°C)

#### Electrical Characteristics (Packaged into modules, unless otherwise specified, T<sub>CASE</sub>=25°C)

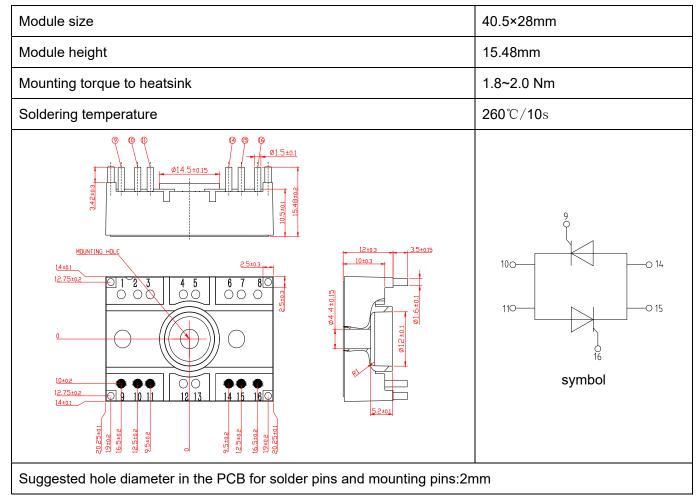
Parameter	Test Conditions	Symbol	Values	Unit
Peak on-state voltage	I⊤=300A tթ=380µs	Vtm	≤1.85	V
Threshold voltage	TJ=125℃	Vто	≤0.9	V
Dynamic resistance	TJ=125℃	R₀	≤3.5	mΩ



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Repetitive peak off-state current	V <b>D=V</b> RRM Tc=25℃	I <sub>DRM1</sub>	≤100	μΑ
	Tc=125℃	DRM2	≤20	mA
Repetitive peak reverse current	V <sub>R</sub> =V <sub>RRM</sub>			
	Tc <b>=25</b> ℃	IRRM1	≤100	μΑ
	Tc=125℃	I <sub>RRM2</sub>	≤20	mA
Triggering gate current	$V_D=12V R_L=30\Omega$	lgт	20-120	mA
Holding current	I <sub>T</sub> =1A	Ін	≤250	mA
Latching current	Ig=1.2Igт	۱L	≤350	mA
Triggering gate voltage	$V_D=12V R_L=30\Omega$	Vgt	≤1.5	V
Non triggering gate voltage	V <sub>D</sub> =V <sub>DRM</sub> T <sub>J</sub> =125℃	Vgd	≥0.2	V
Critical rate of rise of voltage	V <sub>D</sub> =2/3V <sub>DRM</sub> T <sub>J</sub> =125℃ Gate Open	dv/dt	≥1000	V/µs
Thermal resistance	Junction to case	Rth(j-c)	0.45	°C/W

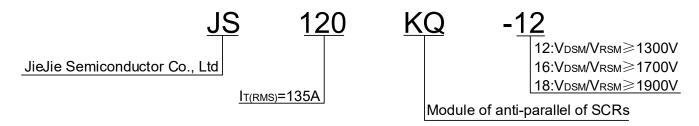
### **Mechanical Characteristics**





## **Anti-parallel Module**

#### **Ordering Information**



#### **Instructions and Precautions**

1) There is no severe vibration and shock in operating environment, and there should be no impurity and atmosphere which may corrode metal and damage the insulation in the air-dielectric.

2) The operating condition of the product can't out of range of the above parameters.

3) When the product is installed on the radiator, the radiator's surface should be confirmed flat, smooth, wipe clean with alcohol, and coated evenly with a layer of thermal grease which thickness is moderate on the contact surface between product and radiator.